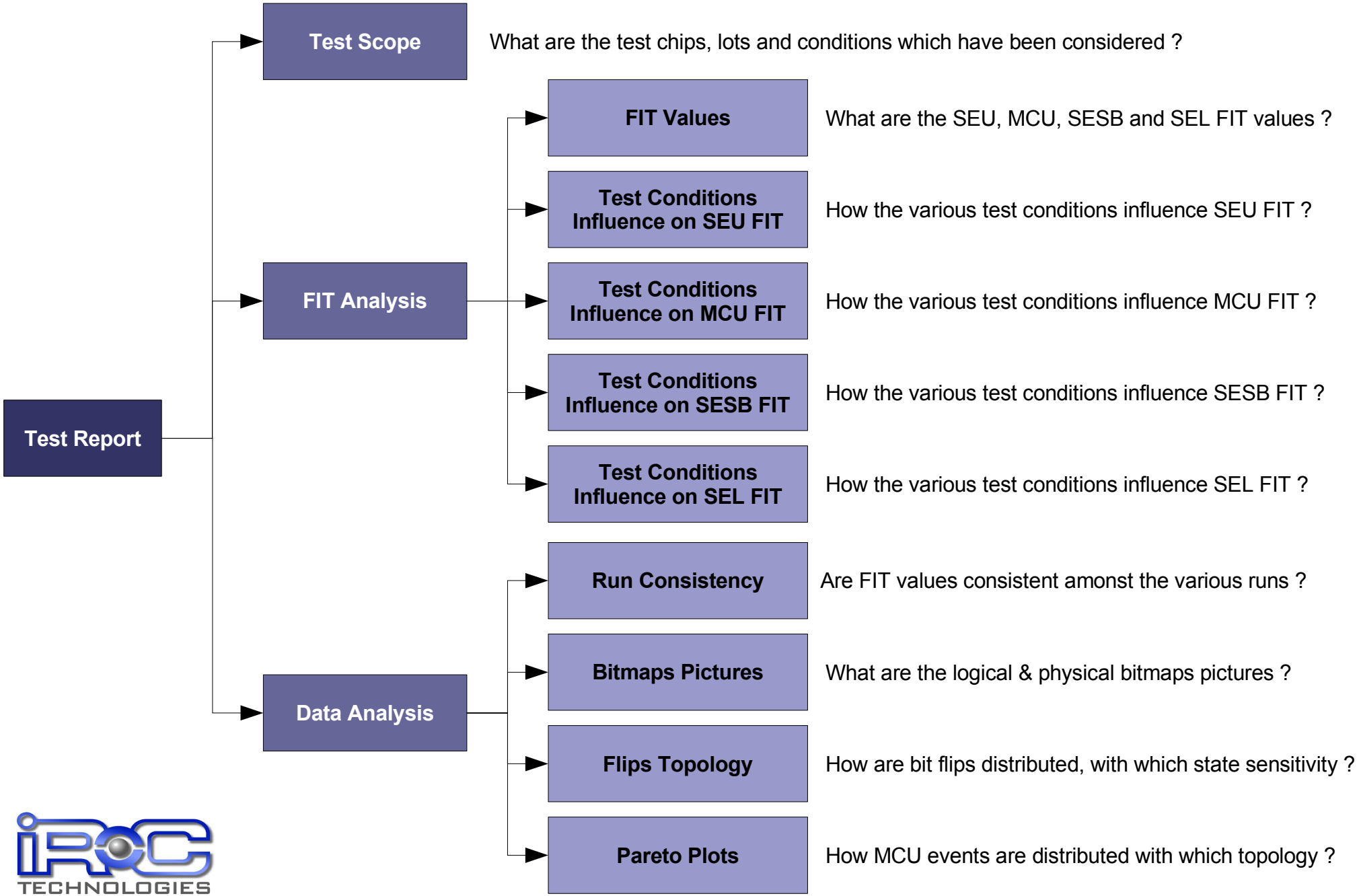
Test Report
Test Scope
What are the test chips, lots and conditions which have been considered ?
FIT Analysis
FIT Values
What are the SEU, MCU, SESB and SEL FIT values ?
Test Conditions Influence on SEU FIT
How the various test conditions influence SEU FIT ?
Test Conditions Influence on MCU FIT
How the various test conditions influence MCU FIT ?
Test Conditions Influence on SESB FIT
How the various test conditions influence SESB FIT ?
Test Conditions Influence on SEL FIT
How the various test conditions influence SEL FIT ?
Data Analysis
Run Consistency
Are FIT values consistent amonst the various runs ?
Bitmaps Pictures
What are the logical & physical bitmaps pictures ?
Flips Topology
How are bit flips distributed, with which state sensitivity ?
Pareto Plots
How MCU events are distributed with which topology ?